

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:  
JACOB et al**

Serial No.: 10/673,678

Confirmation No.: 2021

Filed: September 29, 2003

For: **SYSTEM AND METHOD  
USING EMBEDDED  
MICROPROCESSOR AS A  
NODE IN AN ADAPTABLE  
COMPUTING MACHINE**

Group Art Unit: 2183.

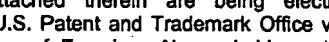
Examiner: Aimee J. Li

**MAIL STOP AMENDMENT**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Examiner:

**CERTIFICATE OF TRANSMISSION**

I hereby certify that this correspondence and the documents referred to as attached therein are being electronically transmitted to the U.S. Patent and Trademark Office via EFS-Web to the attention of Examiner Aimee J. Li, on the date shown below.

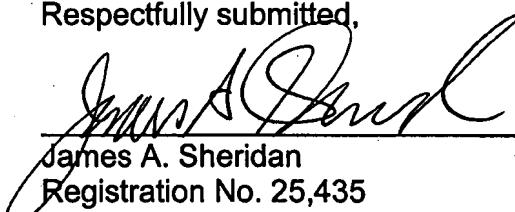
07/30/07   
Date James A. Sheridan, 25,435

**PETITION UNDER 37 C.F.R. §1.47**

Applicant, by his attorney, hereby petitions the U.S. Patent Office to accept the declaration under 37 CFR §131, attached to the response to the Office Action dated April 30, 2007, without a corresponding affidavit or declaration from co-inventor Rojit Jacob. The attached declaration from the attorneys of the inventors, James A. Sheridan, clearly establishes that a diligent search has been made for Mr. Jacob. However, he cannot be found, and he is therefore missing and unavailable to cooperate in the further prosecution of this application. For this reason, acceptance of the declaration of Mr. Chuang as conclusive evidence that the subject matter claimed herein was invented, prior to the publication date of September 26, 2002 of the Master publication (2002/0138716), is respectfully requested.

The Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/NVDA/P002844/JAS for any fees required to make this petition timely and acceptable to the Office

Respectfully submitted,



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Attorney for Applicant(s)

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10673678			
<b>Filing Date:</b>	29-Sep-2003			
<b>Title of Invention:</b>	System and method using embedded microprocessor as a node in an adaptable computing machine			
<b>First Named Inventor/Applicant Name:</b>	Rojit Jacob			
<b>Filer:</b>	James Anton Sheridan/Robyne Orlando			
<b>Attorney Docket Number:</b>	NVDA/P002844			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
<b>Description</b>		<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
Petition fee- 37 CFR 1.17(g) (Group II)	1463	1	200	200
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>200</b>